

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO. 010482	SERIAL NO. Unknown 101092, 868
	APPLICANT Dunworth, et al.	
	FILING DATE March 6, 2002	GROUP Unknown 2682
	DATE MAILED: <u>March 6, 2002</u>	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A1						
	A2						
	A3						
	A4						
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	A9						
	A10						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
	B1						
	B2						
	B3						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

K1	C1	Margarit, et al., "A Low-Noise, Low-Power VCO with Automatic Amplitude Control for Wireless Applications", IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, June 1999, pps. 761 - 771.
	C2	
EXAMINER <i>K. J.</i>		DATE CONSIDERED <u>12/4/04</u>

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Sheet 1 of 3

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 010482	SERIAL NO. 10/092,868
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EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
RP	A1	4 9 2 6 1 4 1	5/15/90	Hirold, et al.			
RP	A2	5 0 7 9 5 2 2	01/07/92	Owen, et al.			
RP	A3	5 2 6 2 9 3 7	11/16/93	Hearn			
RP	A4	5 3 8 2 9 2 2	01/17/95	Gersbach, et al.			
RP	A5	5 4 4 6 4 1 1	08/29/95	Horsfall, et al.			
RP	A6	5 5 7 2 1 6 8	11/05/96	Kasturia			
RP	A7	5 7 0 1 5 9 8	12/23/97	Atkinson			
RP	A8	5 7 4 8 0 4 7	05/05/98	Guthrie, et al.			
RP	A9	5 7 6 7 7 4 8	06/16/98	Nakao			
RP	A10	5 8 0 9 4 0 0	09/15/98	Abramsky, et al.			
RP	A11	5 8 2 8 2 6 6	10/27/98	Couet			
RP	A12	5 8 7 0 0 0 1	02/09/99	Osterling, et al.			
RP	A13	5 9 0 9 6 4 5	06/01/99	Abramsky, et al.			
RP	A14	5 9 9 9 0 6 2	12/07/99	Gilbert			
RP	A15	6 0 0 8 7 0 3	12/28/99	Perrott, et al.			
RP	A16	6 0 2 9 0 5 5	02/22/00	Nobusawa			
RP	A17	6 1 6 7 2 4 5	12/26/00	Welland, et al.			

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EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME
RP	B1	8 1 0 2 6 6 4	04/16/96	JP	Sanyo Electric Co. LTD
RP	B2	2 0 0 0 0 4 9 5 9 7	02/18/00	JP	Asahi Chem Ind. Co. LTD
RP	B3	2 0 0 1 1 5 6 6 3 3	06/08/01	JP	NEC Network Sensa KK

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

RP	C1	"A Space-Qualified Frequency Synthesizer" IEEE (1991)
RP	C2	"Design of an Integrated CMOS PLL Frequency Synthesizer" IEEE
	C3	
	C4	
EXAMINER	Ray J	
DATE CONSIDERED	12/4/04	

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RY	A18	6 1 7 2 5 7 9	01/09/01	Dacus, et al.			
RY	A19	6 1 7 5 2 8 2	01/16/01	Yasuda			
RY	A20	6 2 1 1 7 4 7	04/03/01	Trichet, et al.			
RY	A21	6 2 3 3 4 4 1	05/15/01	Welland, et al.			
RY	A22	6 2 9 2 5 0 7	09/18/01	Hardin, et al.			
RY	A23	6 3 6 9 6 4 7	04/09/02	Main, et al.			
RY	A24	6 4 2 4 2 3 0	07/23/02	Kocaman, et al.			
RY	A25	6 4 6 6 0 9 6	10/15/02	DeVito			
RY	A26	6 6 5 3 9 0 8	11/25/03	Jones			
RY	A27	6 6 6 1 2 9 1	12/09/03	Jovenin			
	A28						
	A29						
	A30						
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	A33						
	A34						

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EXAMINER <i>Ryan</i>	DATE CONSIDERED 12/9/04
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R1	A18	2 0 0 1 0 0 0 9 3 9 1	07/26/01	Kawada, et al.			
R1	A19	2 0 0 2 0 1 6 3 3 9 6	11/07/02	Lim, et al.			
R1	A20	2 0 0 3 0 0 1 1 4 3 7	01/16/03	Ozawa			
RP	A21	2 0 0 3 0 0 2 5 5 6 6	02/06/03	Rogers			
R1	A22	2 0 0 3 0 1 1 9 4 6 7	06/26/03	Welland, et al.			
R1	A23	2 0 0 3 0 1 7 1 1 0 4	09/11/03	Van Rumpf			
R1	A24	2 0 0 3 0 1 7 1 1 0 6	09/11/03	DunWorth, et al.			
R1	A25	2 0 0 3 0 1 8 6 6 6 6	10/02/03	Sindhushayana			
	A26						
	A27						
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